



67,200-367; TSMC 00-407
Serial Number 09/821,554

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TO: ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

FROM: Tung & Associates
838 West Long Lake Road - Suite 120
Bloomfield Hills, MI 48302

DATE: 4 April 2003

REF: Applicant : Chang et al. Filing Date : 29 March 2001
Serial No : 09/821,554 Att'y No. : 67,200-367
Art Unit : 1765 Examiner : Lynette T. Umez-Eronini
Title : Dual Damascene Method Employing Composite Low Dielectric
Constant Dielectric Layer Having Intrinsic Etch Stop Characteristics

AMENDMENT AND RESPONSE TO OFFICE ACTION

Sir:

In response to an office action mailed on 14 January 2003, please consider the following amendments and remarks pertaining to the above referenced application.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington D.C., 20231 on Apr. 2 2003.